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Control Strategy of Flyback Microinverter with Hybrid Mode for PV AC Modules

Sung-Ho Lee, Woo-Jun Cha, Jung-Min Kwon, Member, IEEE and Bong-Hwan Kwon, Member, IEEE

Abstract—This paper presents a control strategy of a flyback microinverter with hybrid operation mode for photovoltaic (PV) AC modules. The proposed control strategy consists of two components: the proportional-resonant (PR) controller with the harmonic compensator (HC), and the hybrid nominal duty ratio. Compared to the conventional control strategy using the proportional-integral (PI) controller, the PR controller with HC provides higher system gain at the fundamental and harmonic frequencies of the grid without using high proportional gain in both operation modes. Then, it enhances tracking speed and disturbance rejection performances satisfying the desired stability. Moreover, by applying the hybrid nominal duty ratio yielded from the proposed operation mode selection, the disturbance rejection is achieved more effectively, and the control burden is reduced. Finally, the simulation and experiment results were shown to verify the tracking speed and disturbance rejection performances of the proposed control strategy.

Index Terms—Right-half-plane zero, current control scheme, single-stage inverter, PV module.

I. INTRODUCTION

With an increase of attention in renewable energy sources, the PV energy has been widely utilized in various industrial fields. The PV power systems can be classified into centralized, string, and AC module systems [1]. Among them, in the AC module system, a low-power grid-connected inverter called as the microinverter is mounted on a single PV module; it can track the individual maximum power point (MPP), and so reduces power losses by PV module mismatch and partial shading [2]-[4]. Moreover, the AC module system has higher reliability and easier maintenance than those of other PV systems [5]. Thus, with these advantages, the AC module PV system has been recently considered as a trend of the future PV power systems.

The worth of the microinverter is evaluated by its power conversion efficiency, the shape of output current, power density, reliability, and cost [6]-[8]. To meet these requirements, a single-stage flyback inverter topology has been adopted due to its simple circuit structure and potential for high efficiency and reliability. Moreover, the flyback inverter topology has both step-down and step-up functions; this characteristic is suitable for the PV applications where the inverter should operate in wide voltage range.

When the flyback inverter operates under the constant switching frequency, the operation modes can be classified as the discontinuous current mode (DCM) [9]-[14] and continuous current mode (CCM) [15]-[17]. Actually, the PV inverter called as the CCM flyback inverter has both operation modes; it inevitably operates in DCM at the low instantaneous power level or low solar irradiation level although it operates in CCM at all instantaneous power levels for rated average power. Then, it can be regarded that the flyback inverter has hybrid operation mode over whole ac-line period. Compared to a flyback inverter only with DCM, the flyback inverter with hybrid mode has the numerous merits such as higher efficiency with lower current stress, higher power capability, and easier filter design. However, the control input-to-output current transfer function of the flyback inverter in the CCM region has a right half-plane (RHP) zero which results in the limitations of increasing the system gain and controller bandwidth. Since the operating point varies widely in the PV inverter applications, especially, the controller should cover the minimum RHP zero. When a conventional PI controller is applied to the flyback inverter with hybrid mode, the proportional gain is designed to be relatively low for ensuring stability in all operating points [17].

The system gain of the flyback inverter in the DCM region is inherently much low. To achieve fast reference tracking and disturbance rejection performances, the high gain feed-back controller is required in the DCM operation. However, when the conventional PI controller is applied, the control gain is limited by the RHP zero in CCM. As a result, it causes unacceptable power quality and high total harmonic distortion (THD) by the poor control performance in DCM [15]. This is the reason why the use of the flyback inverter with hybrid mode is limited despite its many advantages.

To avoid the mentioned problem, some previous studies in [15], [16] control the primary current instead of controlling the output current because there is no RHP zero in the transfer function for the control input to the primary current. The control approach bypasses the difficulties posed by the RHP zero. However, the power quality is low because this approach controls the output current indirectly.

The PR controller is an alternative of the PI controller. It provides an infinite gain at a selected resonant frequency without using high proportional gain [18]-[20]. Moreover, because the controller has flexibility of selecting the resonant frequency.
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Fig. 1. Circuit diagram of the flyback microinverter.

frequency, adding multiple PR controllers such as the harmonic compensator is possible for compensating the harmonics of the selected fundamental frequency.

In this paper, the current control strategy of the flyback microinverter with hybrid mode is proposed. The proposed control strategy consists of two components: the PR controller with HC and the hybrid nominal duty ratio. The PR controller with HC provides high gain at the fundamental and harmonic frequencies of the grid and achieves the zero-tracking error in both operation modes. The hybrid nominal duty ratio performs as a feedforward control input and is determined by the proposed operation mode selection. By applying the hybrid nominal duty ratio according to proper operation region, it can achieve more effective disturbance rejection and faster dynamics. Thus, the proposed control strategy gives the higher tracking performance and better disturbance rejection in both operation modes and strengthens the many advantages of the flyback inverter with hybrid mode. In Section II, the basic operation of the flyback inverter for each mode is introduced. Section III represents the system dynamic characteristic according to each operation mode, and handles with the problems on the conventional current control with the PI controller. Then, the proposed control strategy is introduced, and its superiority and validity are represented from theoretical analysis. Finally, the simulation and experimental results are shown in Section IV.

II. PRINCIPLE OPERATION OF THE FLYBACK INVERTER WITH HYBRID MODE

Fig. 1 shows a circuit diagram of the flyback microinverter; it consists of an input capacitor Cin, a flyback converter with turn ratio \( N_p : N_s \), a full-bridge type unfolding circuit (\( S_1-S_5 \)), and an output filter. The flyback converter operates under the high switching frequency to convert PV power into rectified current for a PV module. \( V_{g} \) and \( I_{g} \) are the grid voltage and grid current; \( V_{PV} \) and \( I_{PV} \) are the average values of the voltage and current for a PV module. \( V_{g} \) and \( I_{g} \) are the grid voltage and grid current; \( V_{PV} \) and \( I_{PV} \) are the average values of the voltage and current for a PV module. The unfolding circuit works under the grid frequency \( f \) to inject sinusoidal ac current into the grid; switches \( S_1 \) and \( S_3 \) are turned on during the positive half-cycle of the grid voltage \( V_{g} \) while \( S_2 \) and \( S_4 \) are turned on during the negative half-cycle.

A. Steady-state analysis of DCM and CCM operations

Under the constant switching frequency \( f \), the operation modes are classified into DCM and CCM. In DCM, the magnetizing current \( i_{m} \) becomes zero within each switching period \( T_s \), and the transformer \( T \) is completely demagnetized as shown in Fig. 2. When \( S_1 \) is turned on, the primary current \( i_{pri} \) is stored in the magnetizing inductance \( L_m \), and its peak value is expressed as follows:

\[
I_{pri, pk} = \frac{V_{PV}}{L_m} D_{DCM}(t) T_s
\]  

(1)

where \( D_{DCM} \) is the duty ratio in DCM. The energy \( E_{LM} \) stored in \( L_m \) is expressed as

\[
E_{LM} = \frac{1}{2} L_m I_{pri, pk}^2(t) = \frac{(V_{PV} D_{DCM}(t) T_s)^2}{2 L_m}.
\]  

(2)

Assuming lossless operation in the inverter, the power balance equations can be obtained as

\[
V_{PV} I_{PV} = \frac{V_{g} I_{g}}{2} = P_o
\]  

(3)

\[
V_{PV} I_{pri}(t) = V_{g}(t) I_{g}(t) = V_{g} I_{g} \sin^2 \omega t
\]  

(4)

where \( V_{PV} \) and \( I_{PV} \) are the average values of the voltage and current for a PV module. \( V_{g} \) and \( I_{g} \) are the grid voltage and grid current; \( V_{PV} \) and \( I_{PV} \) are the average values of the voltage and current for a PV module.

\[
D_{DCM}(t) = \frac{2}{V_{PV}} \sqrt{P_o L_m f_s} \sin \omega t
\]  

(5)

where \( d_{DCM, pk} \) is the peak value of \( D_{DCM} \). In CCM, \( L_m \) is applied to \( V_{PV} \) during the turn-on time of \( S_1 \), while the voltage across \( L_m \) is reflected the output voltage during the turn-off time. Using the voltage-sections law for \( L_m \), the duty ratio \( D_{CCM} \) in CCM is
calculated as

\[ D_{\text{CCM}}(t) = \frac{V_g}{nV_{\text{PV}} + V_g} \sin \omega t \]  

(6)

The duties \( D_{\text{DCM}} \) and \( D_{\text{CCM}} \) determine the relationship between the input voltage \( V_{\text{PV}} \) and output voltage \( V_o \).

As mentioned above, the peak value of the primary current in DCM is expressed as the equation (1). In case of CCM, as shown in Fig. 2, the peak value of the primary current \( I_{\text{pri, pk, CCM}} \) is equal to the sum of the average magnetizing current \( I_m \) and the ripple component. Because the magnetizing current is the same as the primary current when \( S_1 \) is turned on, its average value can be expressed using the average value of the primary current \( I_{\text{pri}} \) and the duty ratio \( D_{\text{CCM}} \) as

\[ I_m(t) = \frac{I_{\text{pri}}(t)}{D_{\text{CCM}}(t)}. \]  

(7)

Thus, from Fig. 2 and the power relationship in (2), the peak value of the primary current \( I_{\text{pri, pk, CCM}} \) is calculated as

\[ I_{\text{pri, pk, CCM}}(t) = \frac{I_{\text{pri}}(t)}{D_{\text{CCM}}(t)} + \frac{V_{\text{PV}}D_{\text{CCM}}(t)T_s}{2L_m}. \]  

(8)

The peak value of the secondary current is the same as the peak value of \( I_{\text{pri}} \) for each mode divided by the turn ratio \( n \).

B. Flyback microinverter with hybrid mode

Under the DCM operation, the turn-off time \( t_{\text{off}} \) is divided into the falling time \( t_f \) and the zero time \( \Delta t \). The time \( t_f \) is constant and is given by [6]

\[ t_f = \frac{nV_{\text{PV}}d_{\text{DCM, pk}}}{V_g}T_s = n\lambda d_{\text{DCM, pk}}T_s \]  

(9)

where \( V_{\text{PV}}/V_g \) is denoted as \( \lambda \). Because \( t_o \) is zero before end of each switching period \( T_s \), the following condition (10) is satisfied in DCM

\[ t_{\text{on}}(t) + t_f = d_{\text{DCM, pk}}\left(\sin \omega t + n\lambda\right)T_s \leq T_s. \]  

(10)

With an increase of the output power, \( D_{\text{DCM}} \) also increases, and the sum of the turn-on time and falling time becomes \( T_s \). Thus, from (5) and (10), the critical duty ratio \( D_{\text{cri}} \) can be obtained as follows:

\[ D_{\text{cri}} = \frac{t_{\text{on}}(t) + t_f}{T_s}. \]  

To make the flyback inverter only operate in the DCM region, \( L_m \) should be lower than \( L_{m,\text{cri}} \) at a certain output power. The flyback inverter only with the DCM region suffers from the high current stress which causes high power losses and limits the increase of the power capacity. As \( L_m \) increases, the CCM region increases, and the maximum current stress gradually decreases. Thus, the high \( L_m \) enhances the efficiency and power capacity. However, when setting the value of \( L_m \), there is a trade-off between the efficiency and transformer size; higher \( L_m \) gives the lower current stress but a larger transformer size. Thus, the design of \( L_m \) for the flyback inverter with hybrid mode should be above \( L_{m,\text{cri}} \), and consider the acceptable current stress and transformer size.
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III. ANALYSIS FOR CONTROL STRATEGY OF FLYBACK MICROINVERTER WITH HYBRID MODE

A. Control issues

In the flyback microinverter with hybrid mode, the current controller should ensure the reference tracking and disturbance rejection performances in both operation regions. Fig. 4 shows the equivalent circuit of the grid-connected flyback micro-inverter. Using the control input-to-output current transfer function introduced in [15], the transfer function $G_{id, DCM}$ in DCM can be expressed as follows:

$$G_{id, DCM} = \frac{V_{PV}}{V_{g, rms}} \frac{P_D}{2L_m f_s}$$  \hfill (13)

where $V_{g, rms}$ is the rms value of the grid voltage. Eqn. (13) is noted that the system gain in DCM is constant and very low at all frequency ranges. Using a small signal modeling, the transfer function $G_{id, CCM}$ in CCM can be represented as

$$G_{id, CCM} = \frac{As + B}{R_{in} C_{in} L_m s^2 + L_m s + D_{CCM}^2 R_{PV}} - \frac{I_{Lm}}{n}$$

where

$$A = R_{PV} C_{in} \frac{(1-D_{CCM})(V_{Cin} + \frac{V_g}{n})}{n}$$

$$B = \frac{1-D_{CCM}}{n} (V_{Cin} + \frac{V_g}{n} - D_{CCM} I_{Lm} R_{PV}).$$

From (14), it is observed that the control input-to-output current transfer function in CCM has an RHP zero. The RHP zero varies according to the operating points, and its minimum value is at the peak of the grid voltage under maximum output power. Thus, the minimum RHP zero should be considered when the controller for the flyback inverter with hybrid mode is designed.

In the conventional control system [17], the PI controller is used to ensure the reference tracking and disturbance rejection performances. Fig. 5 shows open-loop Bode plots of the compensated systems by the conventional PI controller. The operating point in DCM is at the instantaneous power 25W under the rated average output power where it is the minimum RHP zero. The proportional gain $k_p$ of the PI controller is tuned to be low to ensure the stability in the operation point with the minimum RHP zero. Thus, the gains of the conventional PI controller are set as follows: the proportional integral gain $k_p = 0.08$ and $k_i = 64$. As shown in Fig. 5, the system gain at the fundamental and its harmonic frequencies of the grid in DCM is much lower than that in CCM although the integral action of the PI controller gives high DC gain at the origin. This makes the flyback inverter in the DCM region be unable to ensure tracking the reference and rejecting disturbances by the PV and grid voltages effect. To increase the system gain at those frequencies, a high proportional gain is required. However, it raises the system gain at all frequencies and so could make the flyback inverter in the CCM region become unstable. Consequently, when applying the conventional PI controller to the flyback inverter with hybrid mode, there is a trade-off between control performance in DCM and stability in CCM.
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B. Proposed control strategy

To satisfy the desired control performance and stability in both operation modes, the PR controller can be developed, and its transfer function is expressed as

\[ C_{PR}(s) = k_p + \frac{k_r s}{s^2 + \omega_c^2} \]  

(15)

where \( k_r \) is the resonant gain. The PR controller in (15) has an infinite gain at the grid frequency. However, the infinite gain would degrade the control performance and even cause the system to become unstable. In the practical implementation, the following form of the PR controller can be adopted as

\[ C_{PR}(s) = k_p + \frac{2k_r \omega_c s}{s^2 + 2\omega_c s + \omega_c^2} \]  

(16)

where \( \omega_c \) is chosen to widen the controller bandwidth and determines the -3 dB cutoff frequency of the controller. That is, the magnitude of the compensator becomes \( k_r / \sqrt{2} \) at \( \omega - \omega_c \) or \( \omega + \omega_c \). In the PR controller, \( k_r \) is selected in the same way as that of a PI controller. That is, it is designed by the stability of the compensated systems considering the minimum RHP zero. The gain \( k_r \) is tuned relatively high to minimize steady-state error but it is limited by the stability. In case of the proposed PR controller, the gains \( k_r \) and \( \omega_c \) are 20 and 16, respectively; \( k_r \) is selected to make the system have the phase margin above 45° to ensure the stability in practice. In addition, the harmonic compensator is able to alleviate errors for the selective harmonic frequencies, and its transfer function is represented as

\[ C_{HC}(s) = \sum_{h=3,5,7} \frac{2k_{rh} \omega_h s}{s^2 + 2\omega_h s + (h\omega)^2} \]  

(17)

where \( h \) is the harmonic order, and \( k_{rh} \) is the resonant gain for each harmonic frequency. The open-loop Bode plots of compensated system by the PR controller with the 3rd to 7th harmonic compensators is shown in Fig. 6. The 3rd, 5th, and 7th harmonics are the most prominent harmonics under the grid environment. Like the PR controller, HC provides the high gain at selected harmonic frequency components, which helps eliminating steady-state error and the disturbance by the selected frequency components. Compared to the compensated system by the conventional PI controller shown in Fig. 5, it is obvious that the PR controller with harmonic compensators brings the higher gain at the fundamental and its harmonic frequencies of the grid without high proportional gain. In addition, the bandwidth in DCM is increased. Thus, it enhances the reference tracking speed and disturbance rejection performances with satisfaction of the desired stability in both operation modes.

The overall proposed control system for the flyback inverter with hybrid mode is shown in Fig. 7; it consists of the PR controller with HC and the nominal duty ratio \( D_n \). \( I_p^* \) is the peak value of the reference grid current (or output current). As a kind of the feedforward control inputs, the nominal duty ratio \( D_n \) eliminates the disturbance effects and reduces the burden of the feedback controller. In [17], only the duty ratio \( D_{com} \) in (6) is applied to whole ac line period. In this case, the duty ratio \( D_{com} \) causes the voltage mismatch in the DCM region, which increases the burden of the feedback controller. Since the system gain in DCM is relatively lower, this burden becomes heavier. To overcome the mismatch in DCM, the duty ratio \( D_{DCM} \) should...
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be applied when the flyback inverter operates in the DCM region; it means that the nominal duty ratio should be determined according to the operation region. To classify the section of operation modes without an additional current sensor, the critical duty ratio in (11) can be used; it is noted that the flyback inverter operates in the DCM region when the following condition is satisfied as

$$D_{DCM}(t) \leq D_{CCM}(t).$$

Thus, the hybrid nominal duty ratio $D_n$ in the proposed control strategy is determined as follows:

$$D_n(t) = \begin{cases} D_{DCM}(t), & \text{if } D_{DCM}(t) \leq D_{CCM}(t) \\ D_{CCM}(t), & \text{if } D_{DCM}(t) > D_{CCM}(t) \end{cases}.$$  

Finally, the proposed hybrid nominal duty ratio can significantly reduce the disturbance effect in both operation modes, and so improve the performance of the feedback controller.

![Fig. 8. Simulation results for the grid current $i_g$ and its reference $i_{g_{ref}}$ when the conventional control system is applied. (a) quarter-load condition. (b) full-load condition.](image)

![Fig. 9. Simulation results for the grid current $i_g$ and its reference $i_{g_{ref}}$ when the proposed control system is applied. (a) quarter-load condition. (b) full-load condition.](image)

<table>
<thead>
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<td>Grid voltage</td>
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<td>Grid frequency</td>
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<td>Rated average output power</td>
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<td>Rectifier diode</td>
<td>$D_i$</td>
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IV. SIMULATION AND EXPERIMENTAL RESULTS

To verify the feasibility and performance of the proposed control strategy, the simulation by a simulator Psim and experiment using the prototype for the flyback microinverter shown in Fig. 1 were conducted. The nominal PV voltage and rated power were set up to 60V and 200W, respectively. The detail system parameters and parts are listed in Table I. Based on the analysis in the Section III, the controller parameters are designed. The prototype was implemented using Microchip, dsPIC33EP512GM604 under 25 kHz sampling frequency.

Fig. 8 shows the output current control performance when the conventional PI controller only with the duty ratio $D_{DCM}$ in (6) is used. When the conventional control system is used, the grid current is distorted in the low power level. If the gain $k_p$ increases to track the reference signal, the system becomes unstable. Especially, the output current is not regulated by the poor tracking and rejecting disturbance performances in the power level only with DCM region over whole ac line period. On the other hand, in the proposed control strategy, it is observed that the output current tracks the reference current and achieves the almost zero tracking error without high proportional gain as shown in Fig. 9.

Fig. 10 shows the experimental waveforms for the grid voltage and current when the proposed control strategy is applied. As shown in Fig. 10, regardless of load conditions, the grid current has an almost perfect sinusoidal form and desired power level. The THD on the grid current is measured as 2.4% under full-load condition. Fig. 11 shows the dynamic performance under load variation. From Fig. 11, it is verified that the proposed control system makes output current well track its desired value under the load transient-state.

Fig. 12 shows the current stress between the flyback inverter with hybrid mode and the flyback inverter operating only in DCM region. To ensure DCM operation for all operating points, the magnetizing inductance of the DCM flyback is set to 11uH. From Fig. 12, it is observed that the flyback with hybrid mode has much lower current stress in both primary and secondary sides than those of the DCM flyback inverter. Fig. 13 represents efficiency comparison for the two flyback inverters according to load conditions; it is obvious that the flyback inverter with hybrid mode has higher efficiency over all load conditions. Its maximum efficiency is measured to be 96.1%.

V. CONCLUSION

The current control strategy of the flyback microinverter with hybrid mode for PV ac module has been introduced and verified by the analysis, simulation, and experimental results. In the proposed control strategy, the PR controller with HC provides the high system gain at fundamental and harmonic
From the simulation and experiment results, it is verified that the proposed hybrid nominal duty ratio yielded from the proposed operation mode selection eliminates the disturbance more effectively and reduces the burden of the feedback controller. In addition, the proposed hybrid control strategy shows faster reference tracking and better disturbance rejection than those of the conventional PI controller. This makes it to be used in the industrial field.

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